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U. S. PATENT DOCUMENTS						
Examinor Initials*	No.	Pocument Number Number-Kind Code ^{3 process}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Clied Decument	Pages, Gulumes, Lines, When Relevant Passages or Relevan Figures Appear	
ON	CA	LIS- 6,710,460	03-23-2004	MOROZUMI		
	CB	US- 2003/0194877	10-16-2003	YAU et al.		
	CC	US- 6,620,721	09-16-2003	LEE		
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		FORE	IGN PATENT DOGU	MENTS		
Examinor initials	City No.	Foreign Petent Document	Publication Data	Number Palation of Applicant of Cited Document	Pagus, Gulumna, Lines, Where Relevant Passagus Or Relevant Agures Appear	
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D~ 6/10/06 Considered Signature EXAMINER: Willed I reference considered, whether of not exterior to in contemisance with MPEP 608, Draw line through challen if not in conformance and not considered, include copy of this form with next communication to applicant. Applicants unique cluster designation number (applicant). See Kinde Codes of USPTO Patent Documents at www.uspin.gov or MPEP 901,04. Sinter Office that issued the document, by the two-latter code (WIPO Standard ST.3). For

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Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. "Mind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.15 if possible. "Applicant is to place a check mark here if English language Translation is etteched.

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V		•			Application Number	10/878,845	
INFO	DRMATIC	NC	DIS	CLOSURE	Filing Date	10-01-2003	
STATEMENT BY APPLICANT				PPLICANT	First Named Inventor	Gyung-Su CHO	
(Use as many shoots as necessary)					Art Unit	2B11	
					Examiner Name	Nadav, Orl	•
Shoot	2		af	2	Atterney Docket Number	OPP030889US	,

Examiner Initials*	Cite No. ¹	include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T2
ON	DA	Stanley WOLF, Ph.D. and Richard N. TAUBER, Ph.D.; Silicon Processing for the VLSI ERA; Volume 1; Process Technology; 2000; Pages 560-564; Latilice Press, Sunset Beach, CA., USA	
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